

# **Attachment I**

**(Pictures of the EUT and Electrical Schemes)**

1.1 PICTURES

IP20

Front 1



Front 2



IP65

Front 1



Front 2



**IP20**

**Left Side**



**Rigt Side**

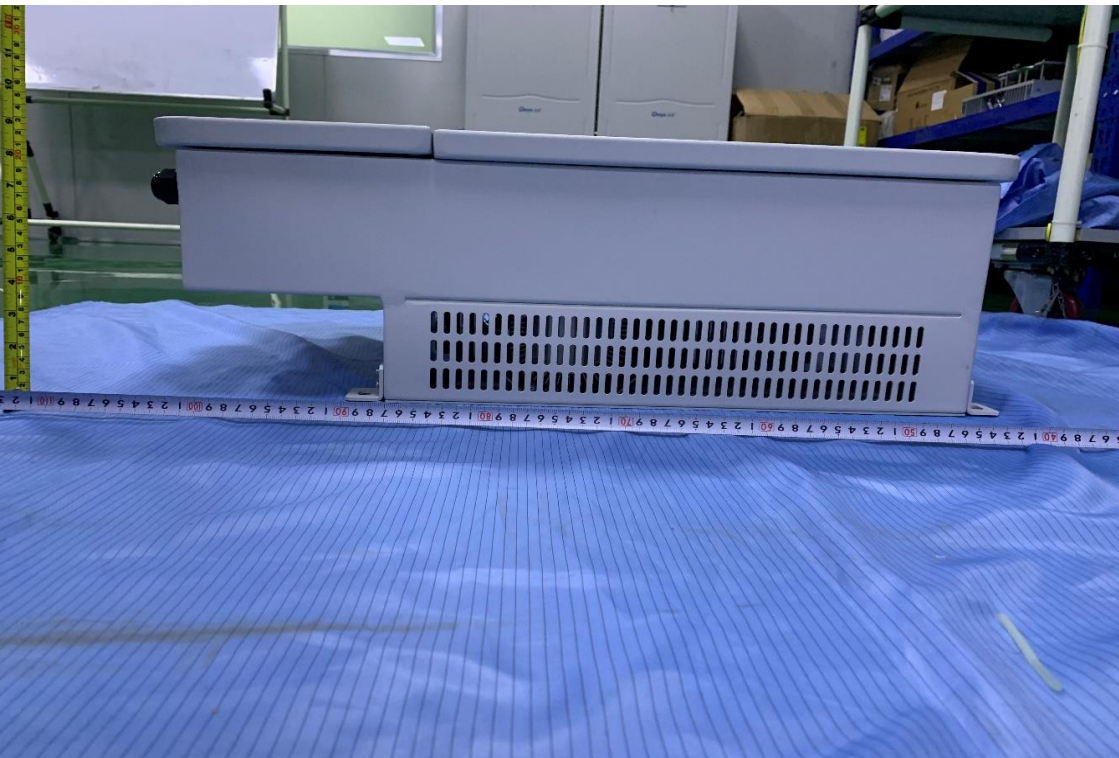


**IP65**

**Left Side**

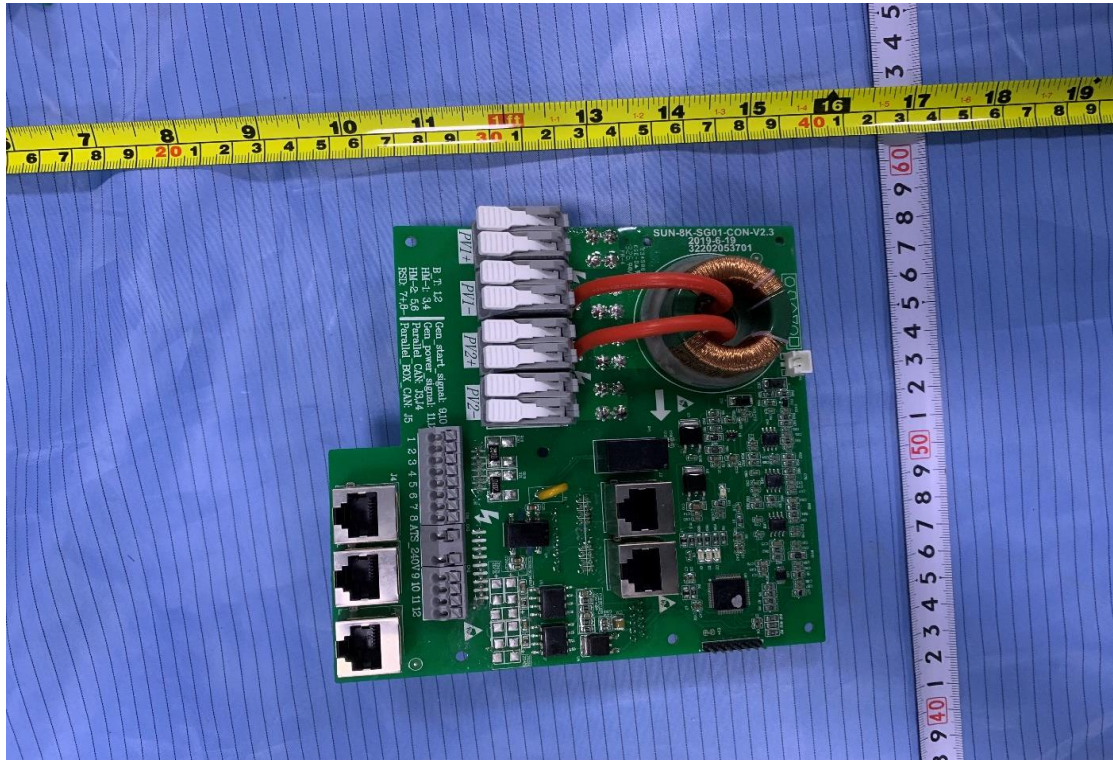


**Rigt Side**

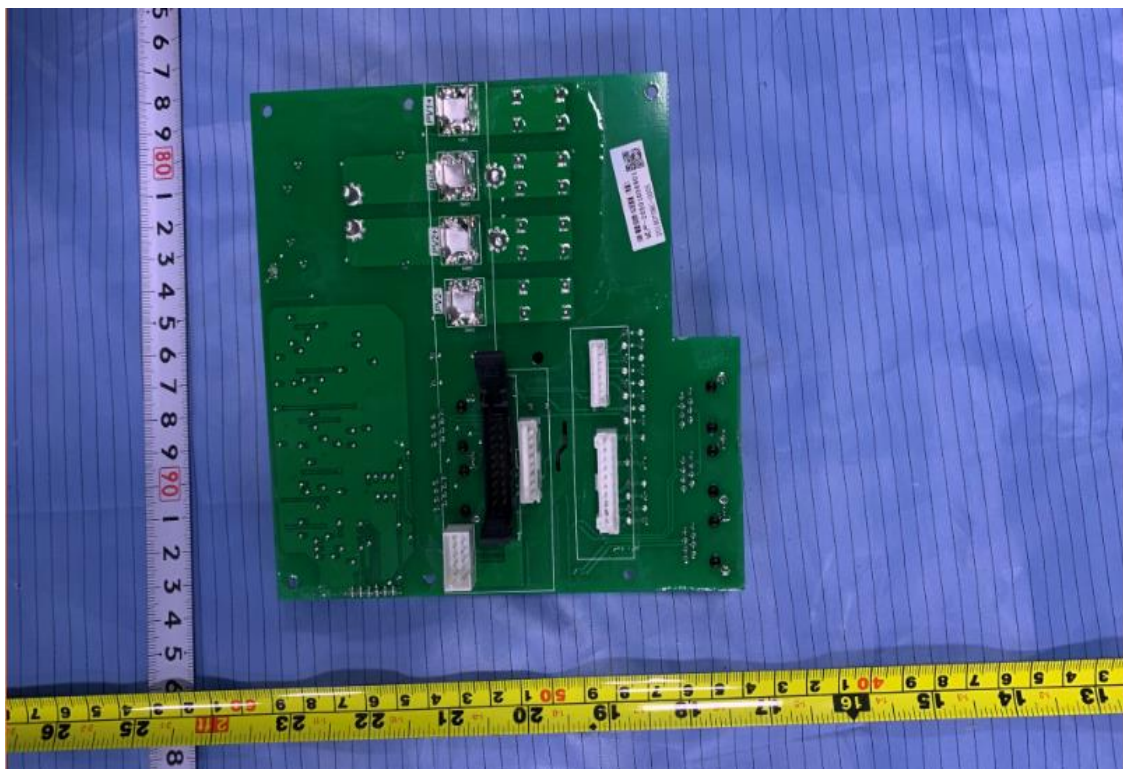


IP20

Front view of ARC Board

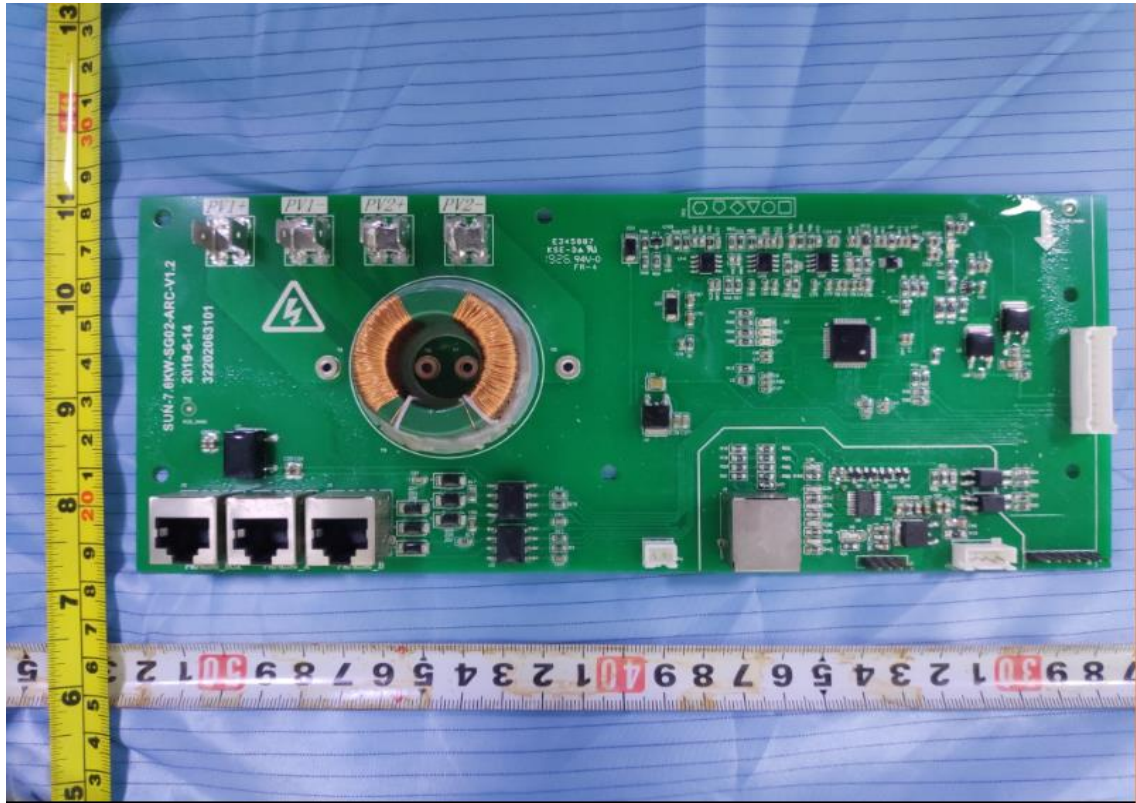


Back view of ARC Board

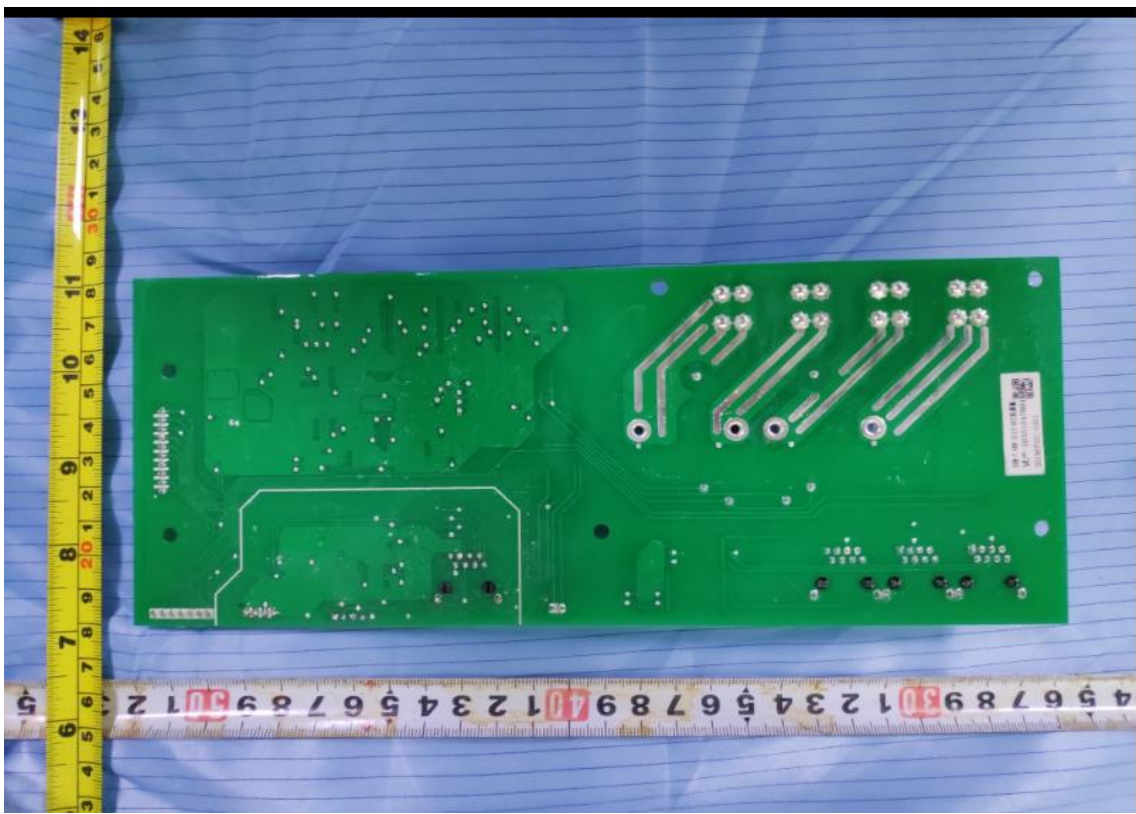


IP65

Front view of ARC Board



Back view of ARC Board

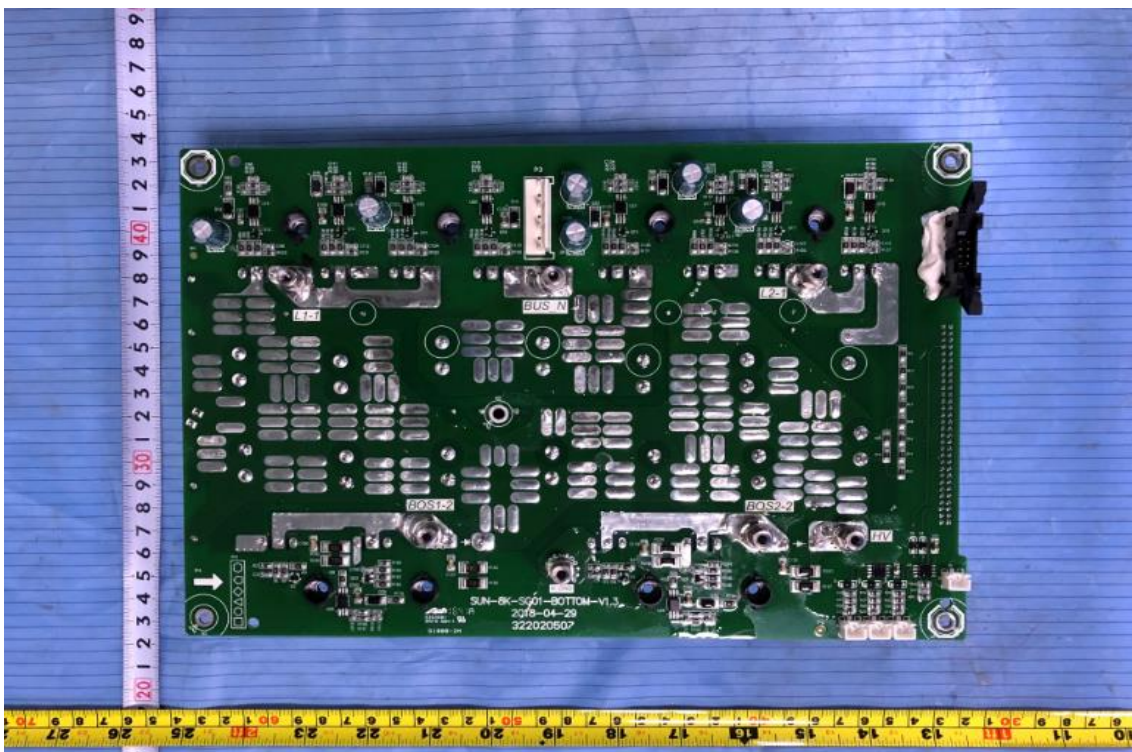


IP20

Front view of Driving Board



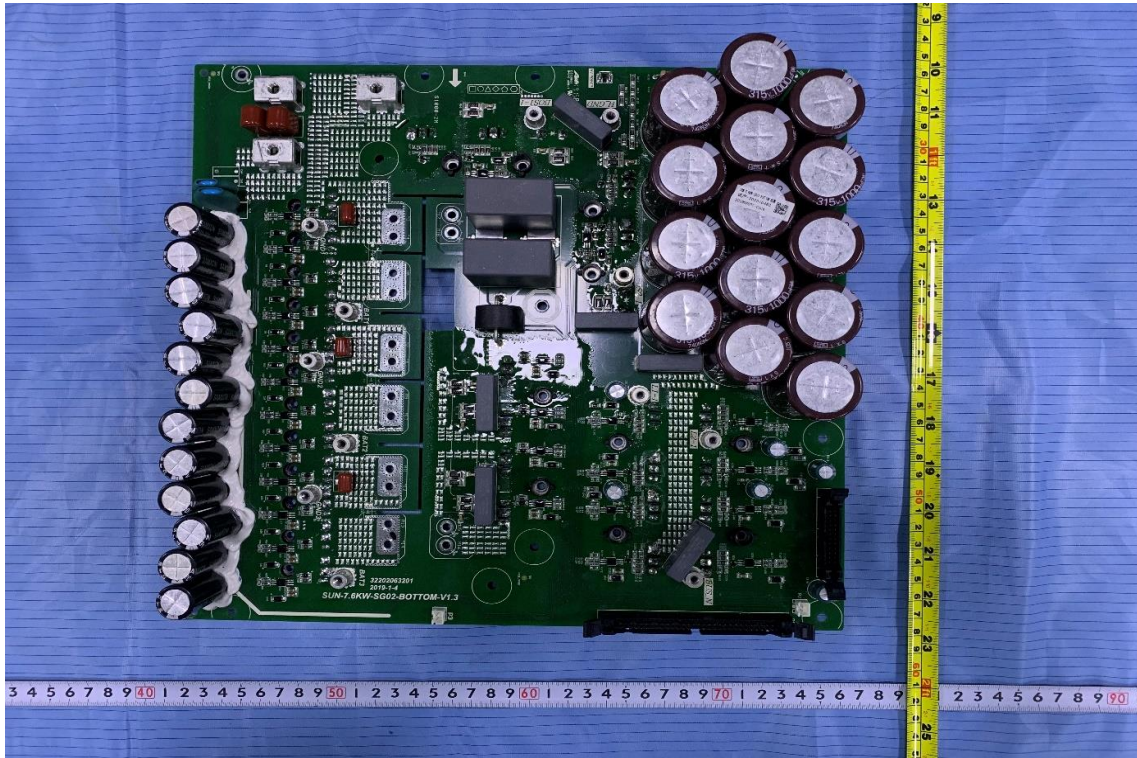
Back view of Driving Board



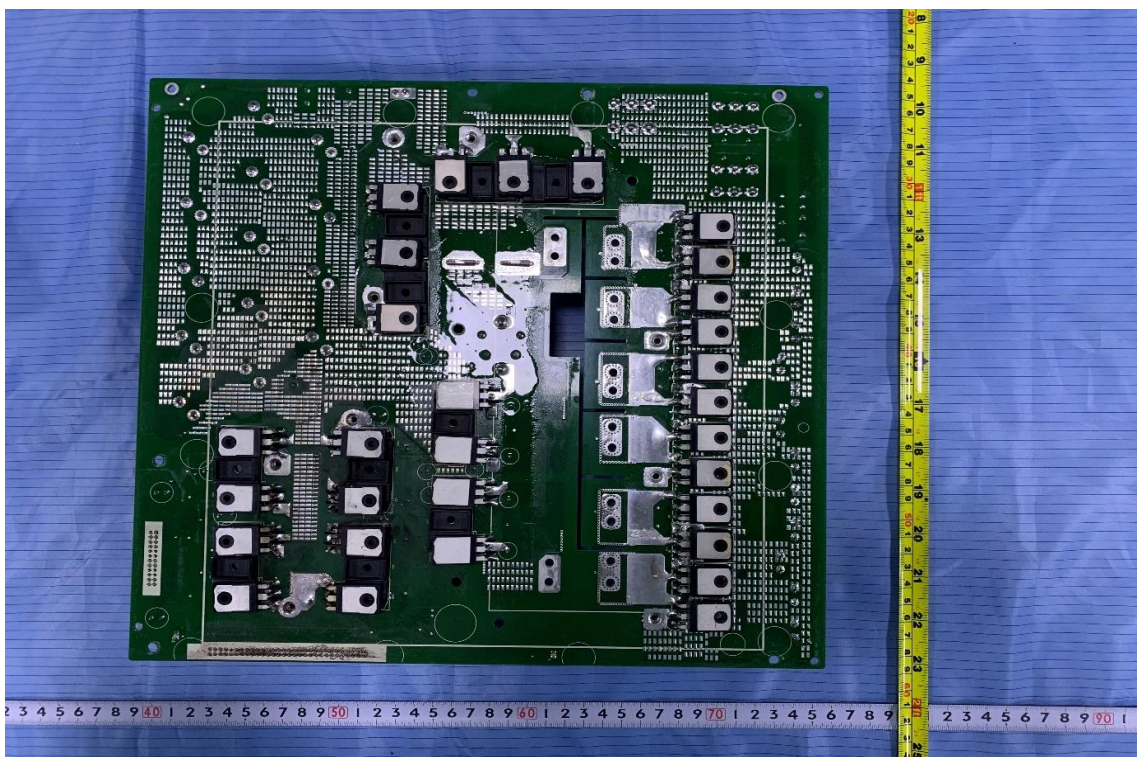


IP65

Front view of Driving Board

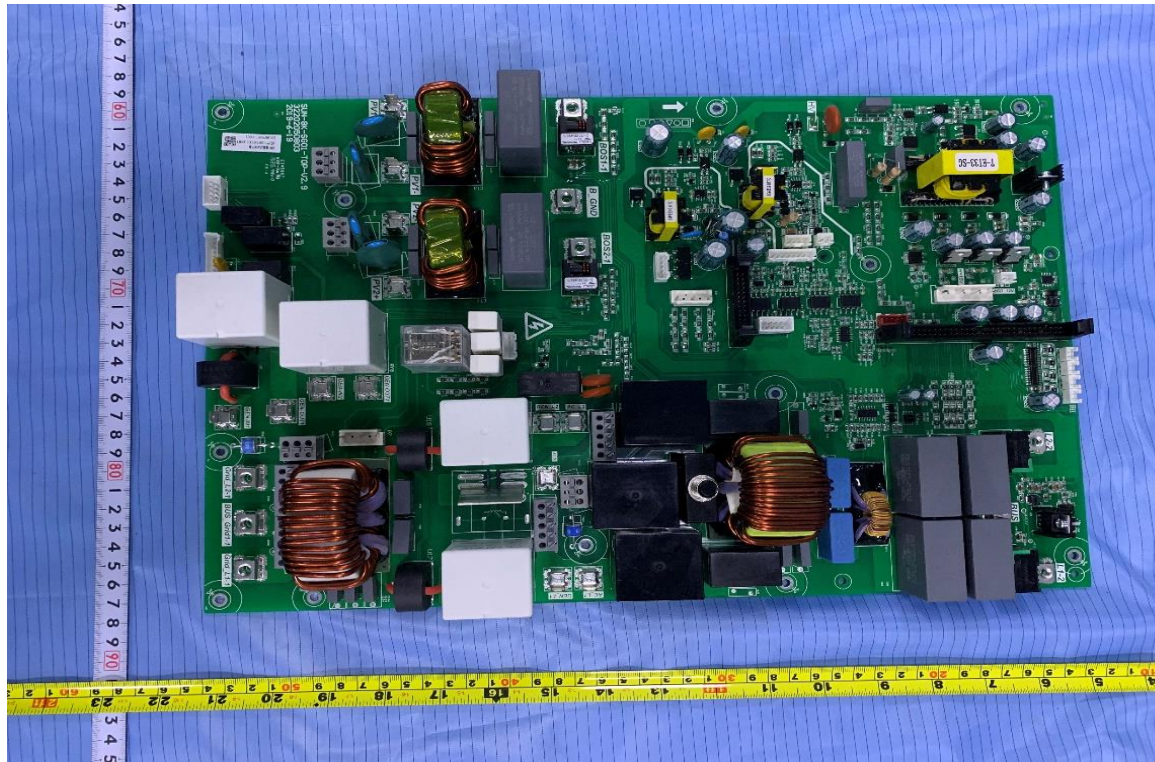


Back view of Driving Board

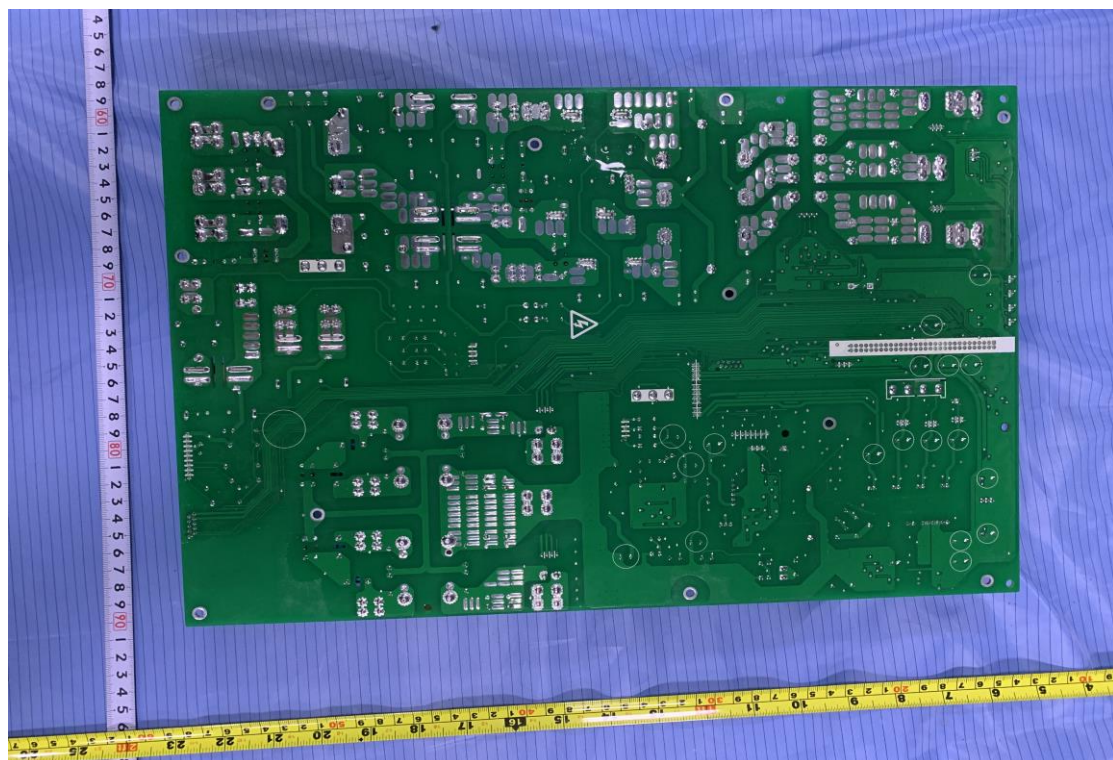


IP20

Front view of Main Board

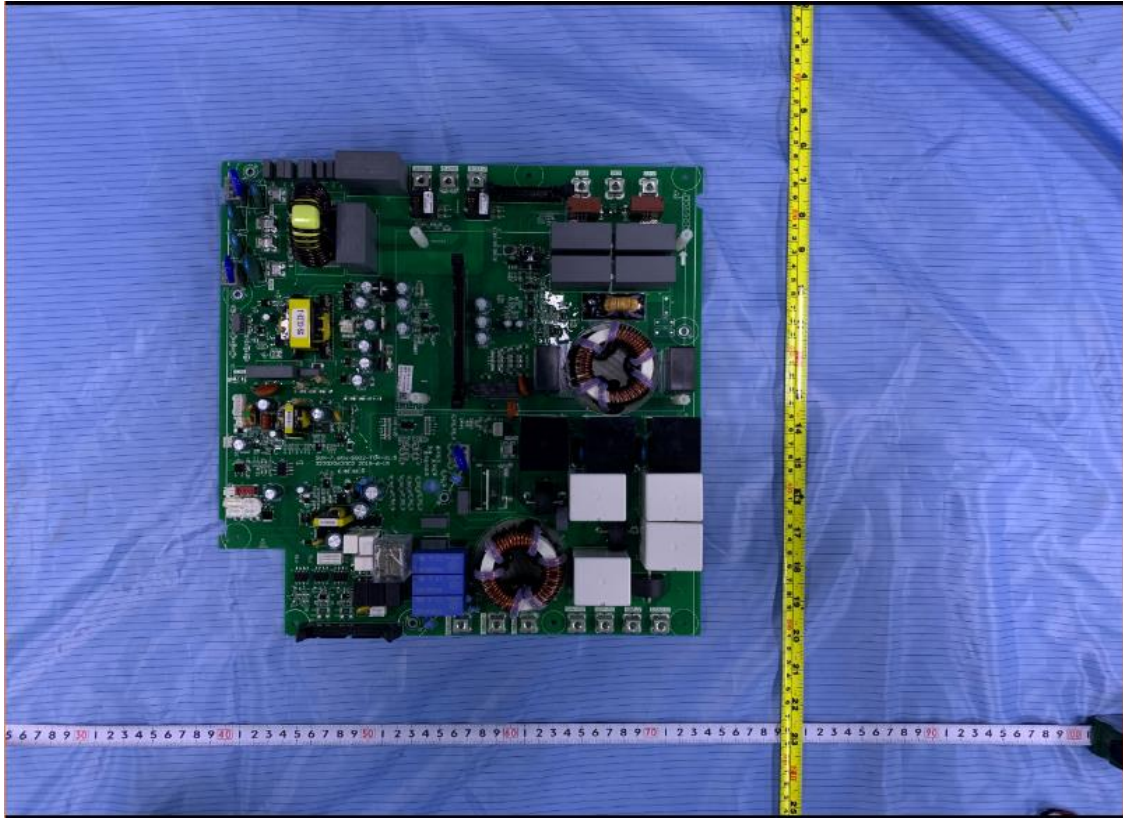


Back view of Main Board

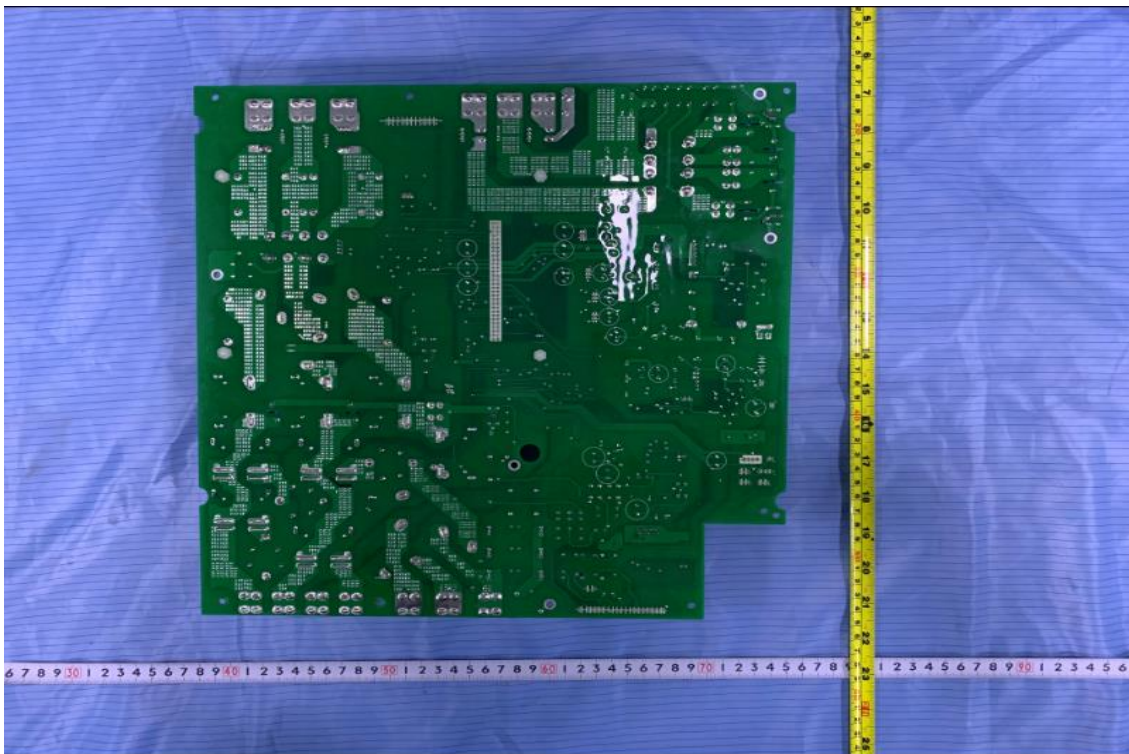


**IP65**

**Front view of Main Board**



**Back view of Main Board**



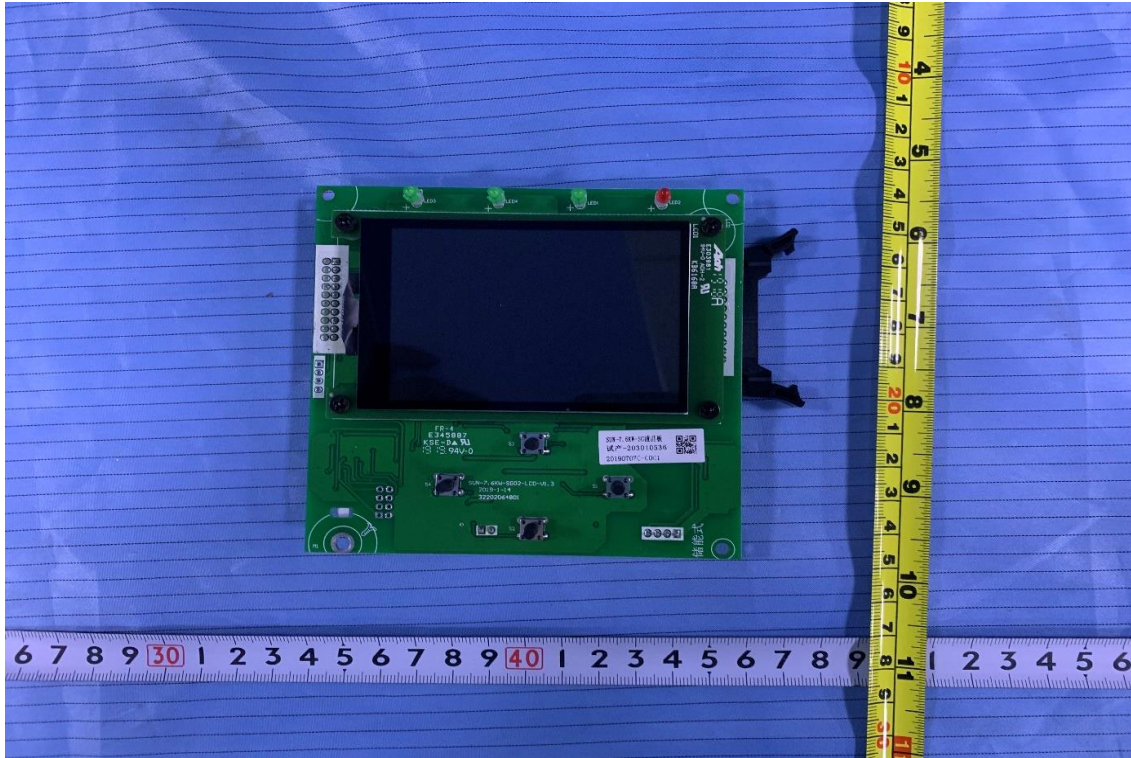
**Front view of Control Board**



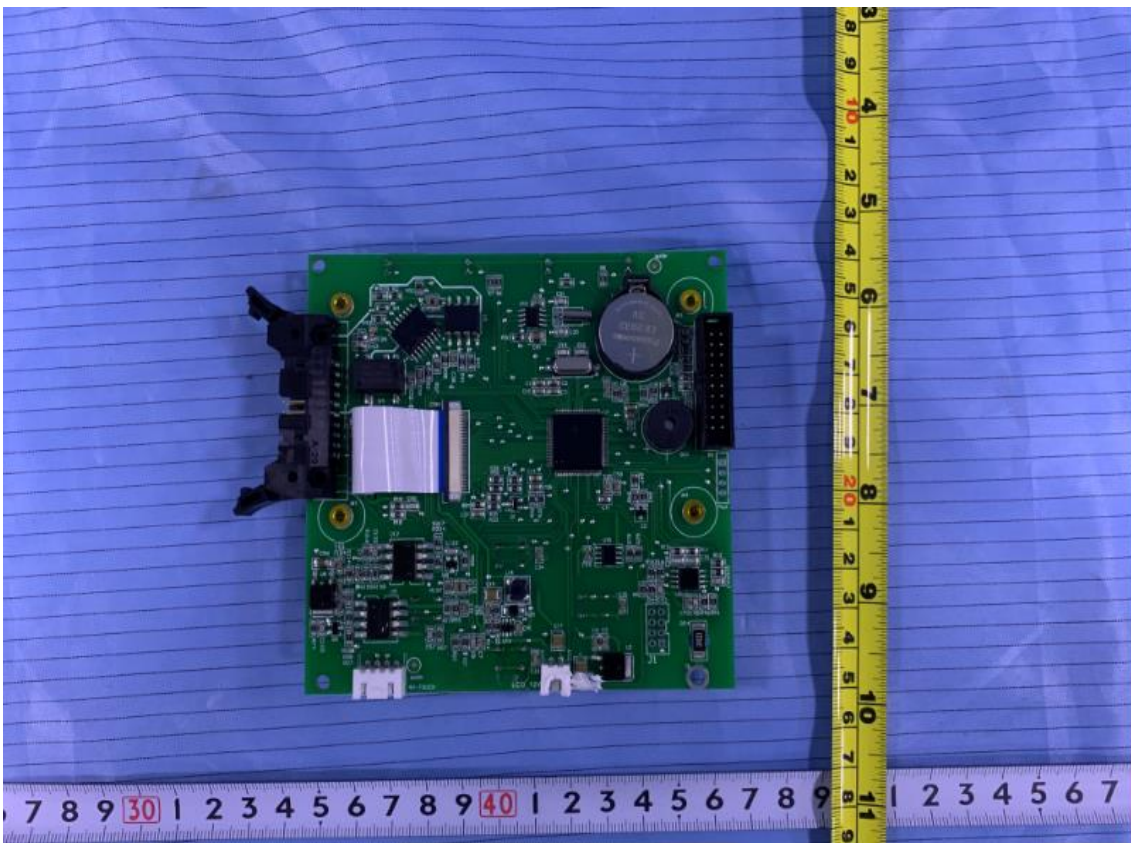
**Back view of Control Board**



Front view of Display Board



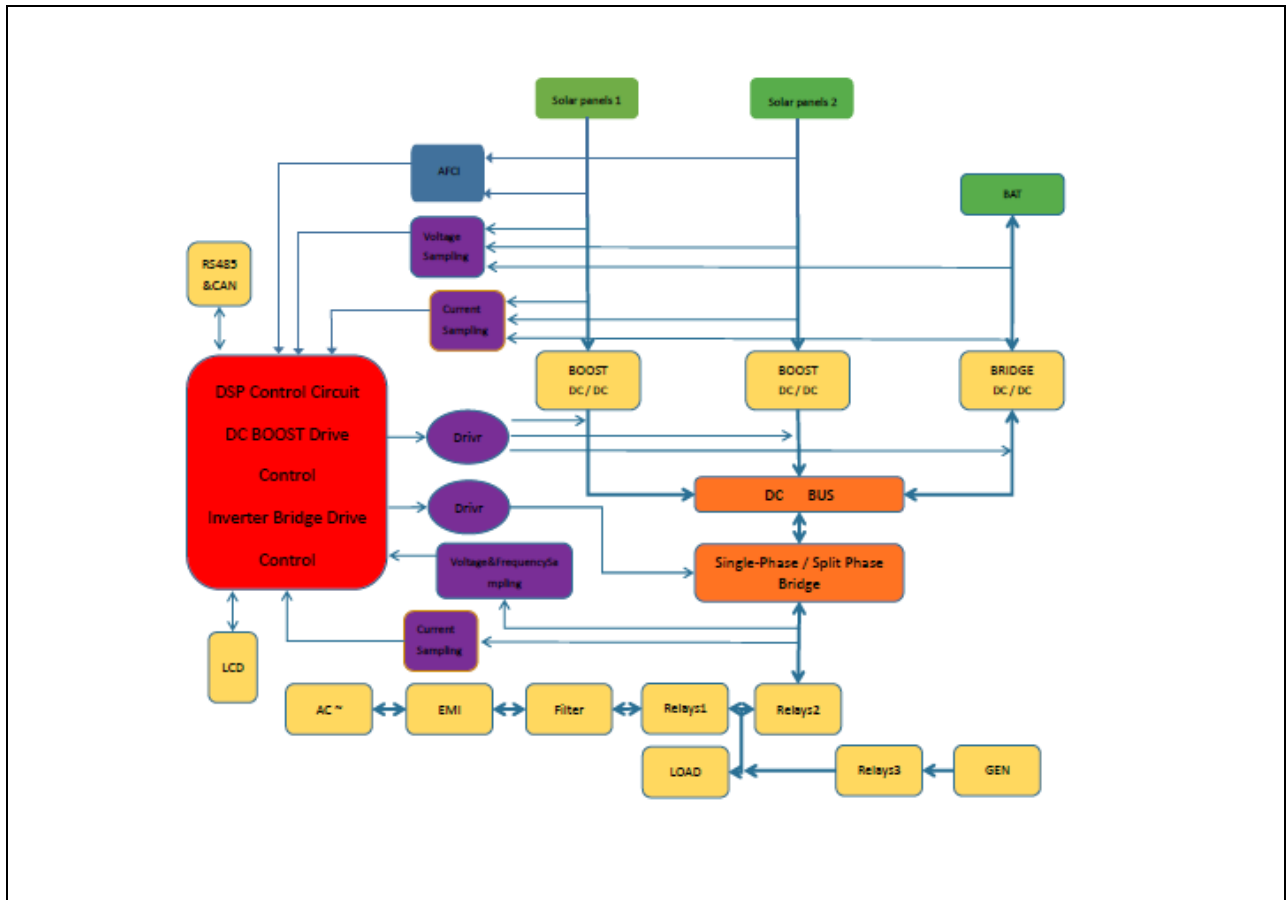
Back view of Display Board



Serial Number and Software Version



1.2 ELECTRICAL SCHEMES

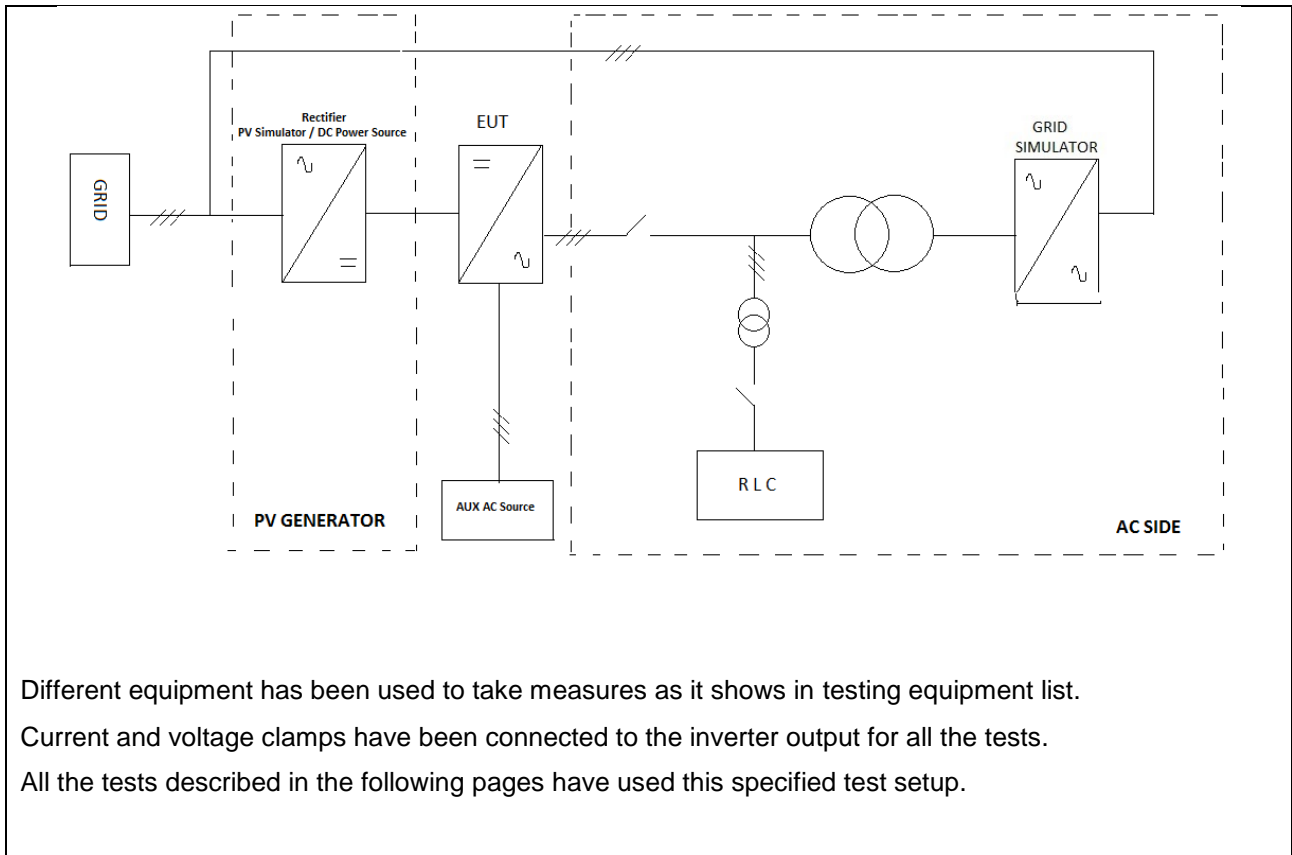


## **Attachment II**

**(Testing information)**



## 2.1 TESTING CIRCUIT



## 2.2 MEASUREMENT UNCERTAINTY

Magnitude	Uncertainty
Voltage measurement uncertainty	±1.5 %
Current measurement uncertainty	±2.0 %
Frequency measurement uncertainty	±0.2 %
Time measurement uncertainty	±0.2 %
Power measurement uncertainty	±2.5 %
Phase Angle	±1°
Temperature	±3° C

Note1: Measurements uncertainties showed in this table are maximum allowable uncertainties. The measurement uncertainties associated with other parameters measured during the tests are in the laboratory at disposal of the solicitant.